

# LM431A/LM431B/LM431C

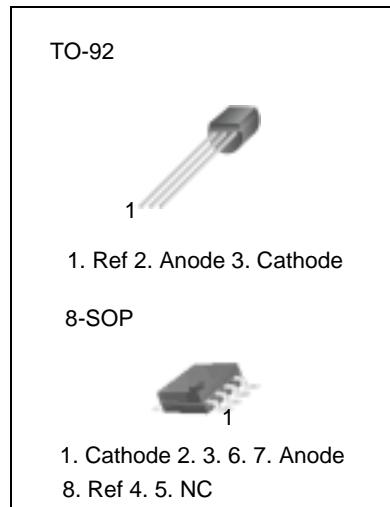
## Programmable Shunt Regulator

### Features

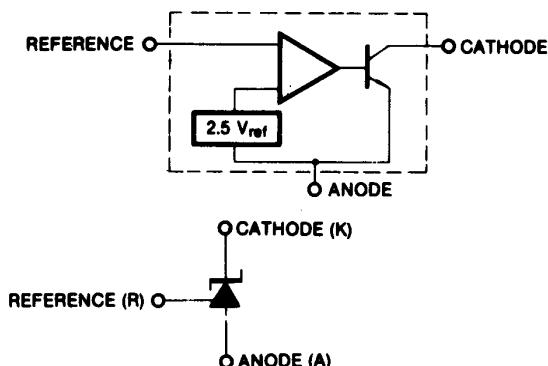
- Output voltage is Programmable to 36 volts
- Low dynamic output impedance 0.2Ω typical
- Sink current capability of 1.0 to 100mA
- Equivalent full-range temperature coefficient of 50ppm/°C typical
- Temperature compensated for operation over full rated operating temperature range
- Low output noise voltage
- Fast turn-on response

### Description

The LM431A/LM431B/LM431C are three-terminal output adjustable regulators with thermal stability over operating temperature range. The output voltage can be set any value between VREF (approximately 2.5 volts) and 36 volts with two external resistors. These devices have a typical dynamic output impedance of 0.2Ω. Active output circuit provides a sharp turn-on characteristic, making these devices excellent replacement for Zener Diodes in many applications.



### Internal Block Diagram



## Absolute Maximum Ratings

(Operating temperature range applies unless otherwise specified.)

Parameter	Symbol	Value	Unit
Cathode Voltage	V <sub>KA</sub>	37	V
Cathode current Range (Continuous)	I <sub>KA</sub>	-100~ + 150	mA
Reference Input Current Range	I <sub>REF</sub>	0.05~ + 10	mA
Power Dissipation D, Z Suffix Package N Suffix Package	P <sub>D</sub>	770 1000	mW mW
<b>Operating Temperature Range</b>			
LM431xC	TOPR	-25 ~ + 85	°C
LM431xl		-40 ~ + 85	°C
Storage Temperature Range	T <sub>STG</sub>	-65 ~ + 150	°C

## Recommended Operating Conditions

Parameter	Symbol	Min	Typ	Max	Unit
Cathode Voltage	V <sub>KA</sub>	V <sub>REF</sub>	-	36	V
Cathode Current	I <sub>KA</sub>	1.0	-	100	mA

## Electrical Characteristics

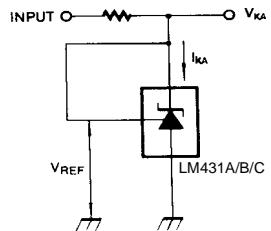
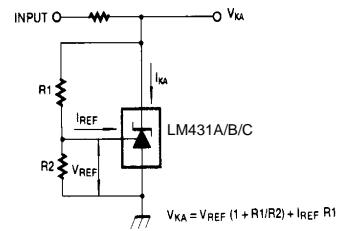
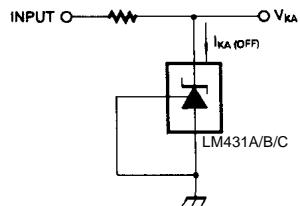
( $T_A = +25^\circ\text{C}$ , unless otherwise specified)

Parameter	Symbol	Conditions	LM431A			LM431B			LM431C			Unit	
			Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.		
Reference Input Voltage	$V_{REF}$	$V_{KA}=V_{REF}$ , $I_{KA}=10\text{mA}$	2.440	2.495	2.550	2.470	2.495	2.520	2.482	2.495	2.508	V	
Deviation of Reference Input Voltage Over-Temperature (Note 1)	$\Delta V_{REF}/\Delta T$	$V_{KA}=V_{REF}$ , $I_{KA}=10\text{mA}$ $T_{MIN} \leq T_A \leq T_{MAX}$	-	4.5	17	-	4.5	17	-	4.5	17	mV	
Ratio of Change in Reference Input Voltage to the Change in Cathode Voltage	$\Delta V_{REF}/\Delta V_{KA}$	$I_{KA} = 10\text{mA}$	$\Delta V_{KA}=10\text{V}-V_{REF}$	-	-10	-2.7	-	-1.0	-2.7	-	-1.0	-2.7	mV/V
			$\Delta V_{KA}=36\text{V}-10\text{V}$	-	-0.5	-2.0	-	-0.5	-2.0	-	-0.5	-2.0	
Reference Input Current	$I_{REF}$	$I_{KA}=10\text{mA}$ , $R_1=10\text{K}\Omega, R_2=\infty$	-	1.5	4	-	1.5	4	-	1.5	4	$\mu\text{A}$	
Deviation of Reference Input Current Over Full Temperature Range	$\Delta I_{REF}/\Delta T$	$I_{KA}=10\text{mA}$ , $R_1=10\text{K}\Omega, R_2=\infty$ $T_A = \text{Full Range}$	-	0.4	1.2	-	0.4	1.2	-	0.4	1.2	$\mu\text{A}$	
Minimum Cathode Current for Regulation	$I_{KA(MIN)}$	$V_{KA}=V_{REF}$	-	0.45	1.0	-	0.45	1.0	-	0.45	1.0	mA	
Off - Stage Cathode Current	$I_{KA(OFF)}$	$V_{KA}=36\text{V}$ , $V_{REF}=0$	-	0.05	1.0	-	0.05	1.0	-	0.05	1.0	$\mu\text{A}$	
Dynamic Impedance	$Z_{KA}$	$V_{KA}=V_{REF}$ , $I_{KA}=1 \text{ to } 100\text{mA}$ $f \geq 1.0\text{KHz}$	-	0.15	0.5	-	0.15	0.5	-	0.15	0.5	$\Omega$	

### Note1

- LM431xC :  $T_{MIN} = -25^\circ\text{C}$ ,  $T_{MAX} = +85^\circ\text{C}$
- LM431xI :  $T_{MIN} = -40^\circ\text{C}$ ,  $T_{MAX} = +85^\circ\text{C}$

## Test Circuits

Figure 1. Test Circuit for  $V_{KA}=V_{REF}$ Figure 2. Test Circuit for  $V_{KA} \geq V_{REF}$ Figure 3. Test Circuit for  $I_{KA(OFF)}$

## Typical Performance Characteristics

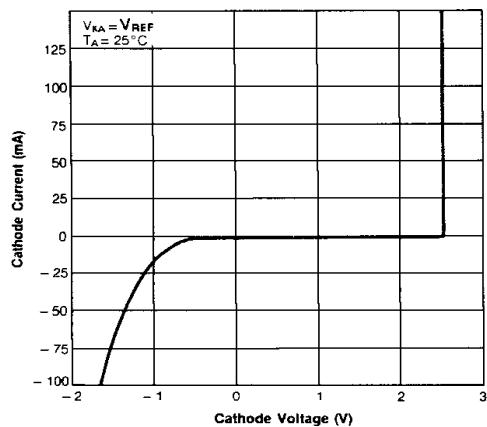


Figure 1. Cathode Current vs. Cathode Voltage

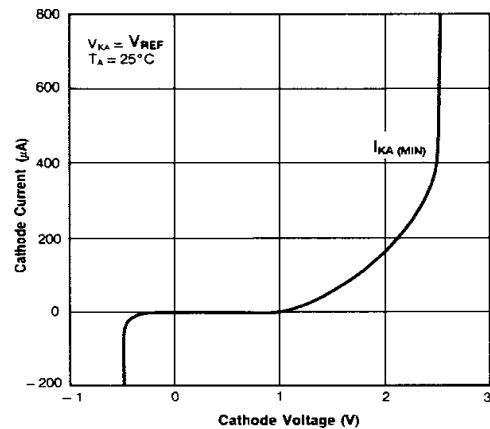


Figure 2. Cathode Current vs. Cathode Voltage

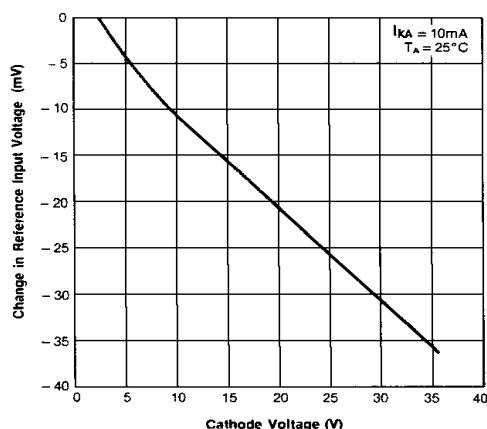


Figure 3. Change In Reference Input Voltage vs. Cathode Voltage

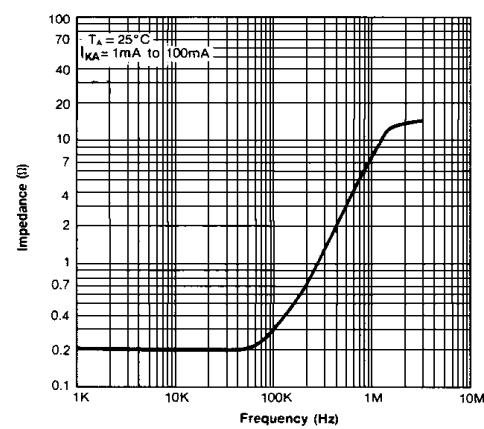


Figure 4. Dynamic Impedance Frequency

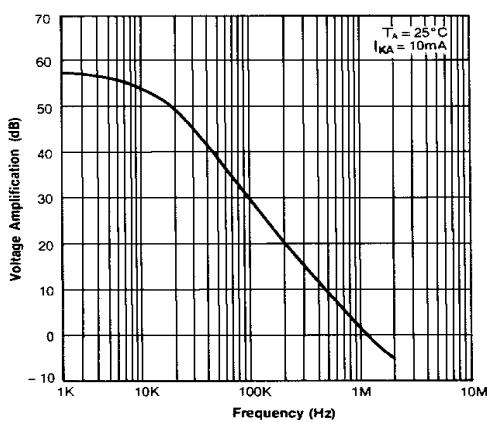


Figure 5. Small Signal Voltage Amplification vs. Frequency

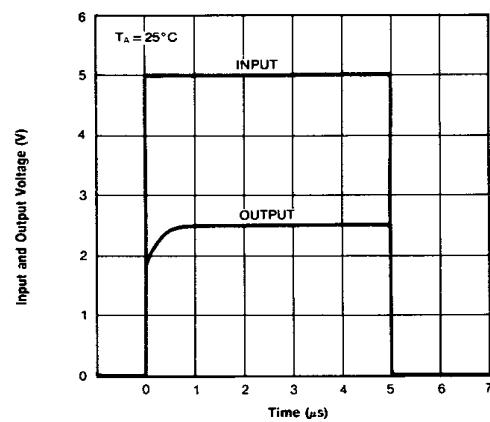


Figure 6. Pulse Response

## Typical Application

$$V_O = \left(1 + \frac{R_1}{R_2}\right)V_{ref}$$

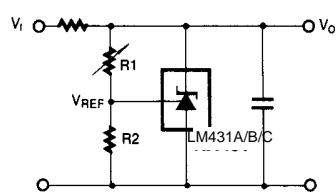


Figure 10. Shunt Regulator

$$V_O = V_{ref} \left(1 + \frac{R_1}{R_2}\right)$$

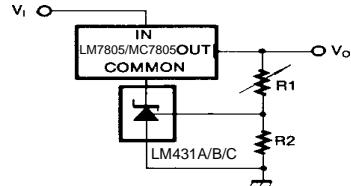


Figure 11. Output Control for Three-Terminal Fixed Regulator

$$V_O = \left(1 + \frac{R_1}{R_2}\right)V_{ref}$$

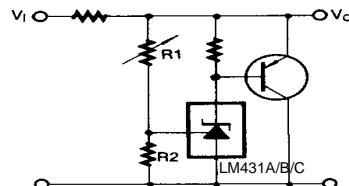


Figure 12. High Current Shunt Regulator

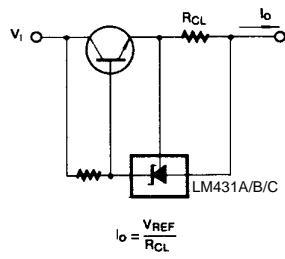


Figure 13. Current Limit or Current Source

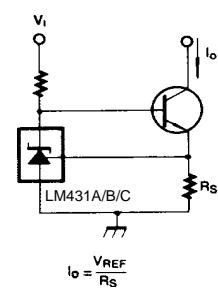
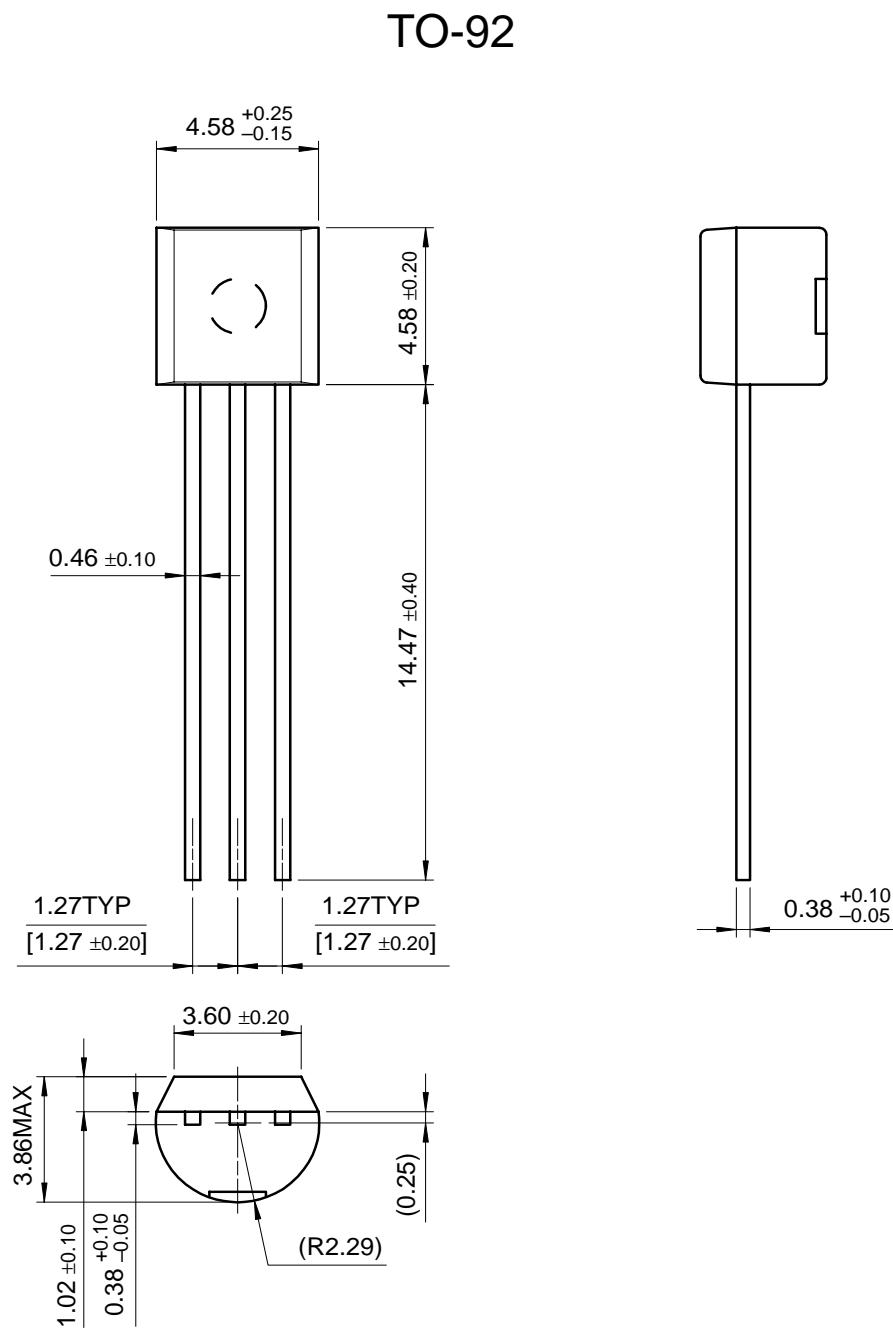


Figure 14. Constant-Current Sink

## Mechanical Dimensions

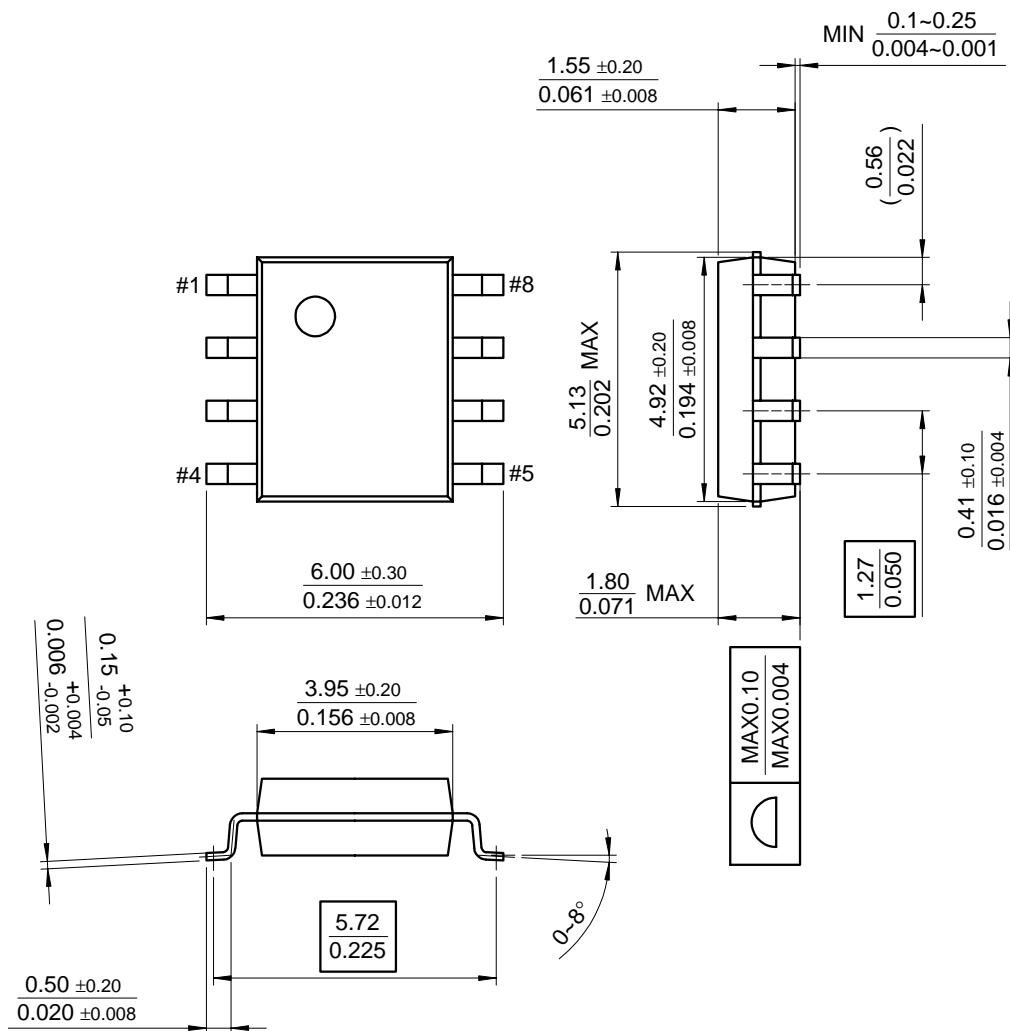
### Package



## Mechanical Dimensions (Continued)

### Package

**8-SOP**



## Ordering Information

Product Number	Output Voltage Tolerance	Package	Operating Temperature
LM431CCZ	0.5%	TO-92	-25 ~ + 85 °C
LM431CCM		8-SOP	
LM431BCZ	1%	TO-92	
LM431BCM		8-SOP	
LM431ACZ	2%	TO-92	
LM431ACM		8-SOP	
LM431CIZ	0.5%	TO-92	-40 ~ + 85 °C
LM431CIM		8-SOP	
LM431BIZ	1%	TO-92	
LM431BIM		8-SOP	
LM431AIZ	2%	TO-92	
LM431AIM		8-SOP	

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