

Medium Power Transistor

2SA1036K

●Features

- 1) Large I_C . $I_{CMAX.} = -500 \text{mA}$
- 2) Low $V_{\text{CE(sat)}}$ Ideal for low-voltage operation.
- 3) Complements the 2SC2411K.

Structure

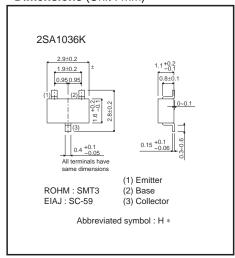
Epitaxial planer type PNP silicon transistor

● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit			
Collector-base voltage	V _{CBO}	-40	V			
Collector-emitter voltage	Vceo	-32	V			
Emitter-base voltage	V _{EBO}	-5	V			
Collector current	Ic	-0.5	A *			
Collector power dissipation	Pc	0.2	W			
Junction temperature	Tj	150	°C			
Storage temperature	Tstg	-55 to +150	°C			

^{*}Pc MAX. must not be exceeded.

●Dimensions (Unit : mm)



* Denotes her

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	BV _{CBO}	-40	-	_	V	I _C = -100μA
Collector-emitter breakdown voltage	BV _{CEO}	-32	_	_	V	I _C = -1mA
Emitter-base breakdown voltage	BV _{EBO}	-5	-	_	V	I _E = -100μA
Collector outoff current	I _{CBO}	_	-	-1	μΑ	V _{CB} = -20V
Emitter cutoff current	I _{EBO}	_	-	-1	μΑ	V _{EB} = -4V
Collector-emitter saturation voltage	VCE(sat)	_	_	-0.6	V	I _C /I _B = -300mA/-30mA
DC current transfer ratio	h _{FE}	120	-	390	-	V _{CE} = -3V, I _C = -100mA
Transition frequency	f⊤	_	200	_	MHz	V _{CE} = -5V, I _E =20mA, f=100MHz
Output capacitance	Cob	_	7	_	pF	V _{CB} = -10V, I _E =0A, f=1MHz

Packaging specifications

		Package	Taping		
		Code	T146		
Type	h _{FE}	Basic ordering unit (pieces)	3000		
2SA1036K	QR		0		

h_{FE} values are classifies as follows.

Item	Q	R
h _{FE}	120 to 270	180 to 390

2SA1036K Data Sheet

•Electrical characteristic curves

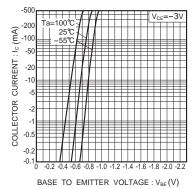


Fig.1 Grounded emitter propogation

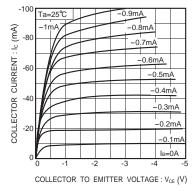


Fig.2 Grounded emitter output characteristics (I)

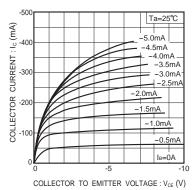


Fig.3 Ground emitter output characteristics (II)

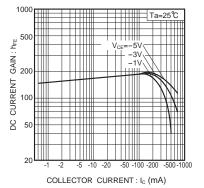


Fig.4 DC current gain vs. collector current (I)

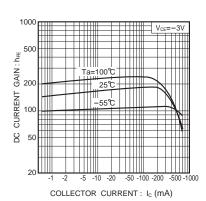


Fig.5 DC current gain vs. collector current (II)

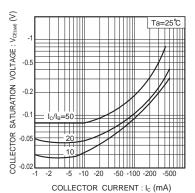


Fig.6 Collector emitter saturation voltage vs. collector current (I)

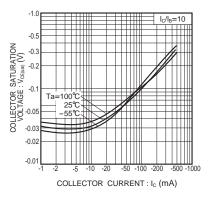


Fig.7 Collector-emitter saturation voltage vs. collector current (II)

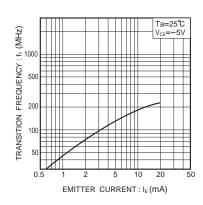
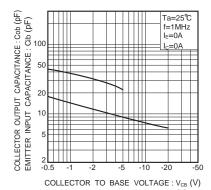


Fig.8 Gain bandwidth product vs. emitter current



 $\label{eq:fig:base} \begin{array}{ll} \text{EMITTER TO BASE VOLTAGE}: V_{\text{EB}}\left(V\right) \\ \text{Fig.9} & \text{Collectur output capacitance vs.} \\ & \text{collector-base voltage. Emitter input capacitance vs. emitter -base voltage} \end{array}$

Notes

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