

8. PIN VOLTAGES OF IC's

IC101(TDA 9345)							
TV signal processor-Teletext decoder with embedded Controller							
PIN	SYMBOL	DESCRIPTION	V DC(*)	PIN	SYMBOL	DESCRIPTION	V DC(*)
1	P1.3/T1	port 1.3 or Counter/Timer 1 input	2,46(2,4)	33	HOUT	horizontal output	0,88(3,12)
2	P1.6/SCL	port 1.6 or I2C-bus clock line	4,88(0)	34	FBISO	flyback input/sandcastle output	0,41(0)
3	P1.7/SDA	port 1.7 or I2C-bus data line	4,88(0)	35	AUDTEXT	external audio input	3,71(-0,06)
4	P2.0/TPMW	port 2.0 or Tuning PWM output	0,30(0)	36	EHTO	EHT/overvoltage protection input	1,72(0)
5	P3.0/ADC0/PWM0	port 3.0 or ADC0 input or PWM0 output	0,15(3,22)	37	PLLIF	IF-PLL loop filter	2,45(0)
6	P3.1/ADC0/PWM1	port 3.1 or ADC1 input or PWM1 output	2,48(0)	38	IFVO/SVO	IF video output / selected CVBS output	3,05(0)
7	P3.2/ADC0/PWM2	port 3.2 or ADC2 input or PWM2 output	1,65(1,62)	39	VP1	main supply voltage TV processor	8,12(0)
8	P3.3/ADC0/PWM3	port 3.3 or ADC3 input or PWM3 output	0(0)	40	CVBS1	internal CVBS input	3,79(0)
9	VSSC/P	digital ground for μ -Controller core and periphery	0(0)	41	GND	ground for TV processor	0(0)
10	P0.5	port 0.5 (8 mA current sinking capability for direct drive of LEDs)	0(2,72)	42	CVBS/Y	CVBS3/Y input	3,33(0)
11	P0.6	port 0.6 (8 mA current sinking capability for direct drive of LEDs)	0(0)	43	C	chroma input	1,54(0)
12	VSSA	digital ground of TV-processor	0(0)	44	AUDOUT	audio output	3,27(0)
13	SECPLL	SECAM PLL decoupling	2,28(0)	45	INSSW2	2nd RGB / YPRPB insertion input	0,03(0)
14	VP2	2nd supply voltage TV-processor (+8V)	8,15(0)	46	R2/P _R IN	2nd R input / PR input	2,54(0)
15	DECDIG	supply voltage decoupling of digital circuit of TV-processor	5,01(0)	47	G2/YIN	2nd G input / Y input	2,54(0)
16	PH2LF	phase-2 filter	2,56(0)	48	B2/P _B IN	2nd B input / PB input	2,54(0)
17	PH1LF	phase-1 filter	3,92(0)	49	BCLIN	beam current limiter input	2,28(0,32)
18	GND3	ground 3 for TV-processor	0(0)	50	BLKIN	black current input / V-guard input	7,13(0)
19	DECBG	bandgap decoupling	3,99(0)	51	RO	Red output	2,81(-0,03)
20	AVL	Automatic Volume Levelling	0,01(0,01)	52	GO	Green output	2,81(-0,03)
21	VDRB	vertical drive B output	0,94(0)	53	BO	Blue output	2,79(-0,03)
22	VDRA	vertical drive A output	0,98(0)	54	VDDA	analog supply of Teletext decoder and digital supply of TV-processor (3.3 V)	3,24(3,24)
23	IFIN1	IF input 1	1,85(0)	55	VPE	OTP Programming Voltage	0(0)
24	IFIN2	IF input 2	1,85(0)	56	VDDC	digital supply to core (3.3 V)	3,27(3,24)
25	IREF	reference current input	3,86(0)	57	OSCGND	oscillator ground supply	0,03(0)
26	VSC	vertical sawtooth capacitor	3,88(0)	58	XTALIN	crystal oscillator input	1,57(1,57)
27	AGCOUT	tuner AGC output	1,46(0)	59	XTALOUT	crystal oscillator output	1,66(1,62)
28	AUDEEM	Audio deemphasis	3,15(0)	60	RESET	reset	0(0)
29	DECSDEM	decoupling sound demodulator	2,21(0,28)	61	VDDP	digital supply to periphery (+3.3 V)	3,27(3,18)
30	GND2	ground 2 for TV processor	0(0)	62	P1.0/INT1	port 1.0 or external interrupt 1 input	3,58(3,48)
31	SNDPLL	narrow band PLL	2,21(0)	63	P1.1/T0	port 1.1 or Counter/Timer 0 input	3,29(3,24)
32	IC	internally connected	0,34(0)	64	P1.2/INT0	port 1.2 or external interrupt 0 input	3,29(3,24)

(*) Stand-by values are given the parenthesis

NOTE: The function of pin 15, 27, 33 and 48 is dependent on the mode of operation (mono intercarrier mode / QSS IF amplifier) and is controlled by some software control bits.3

IC301(TDA 2822)							
TV signal processor-Teletext decoder with embedded Controller							
PIN	SYMBOL	DESCRIPTION	V DC(*)	PIN	SYMBOL	DESCRIPTION	V DC(*)
1	INPUT +(1)	1st Input(Positive)	0(0)	9	N.C.	No Connection	0(0)
2	N.C.	No Connection	0(0)	10	N.C.	No Connection	0(0)
3	INPUT-(1)	1st Input(Negative)	0,5(0,5)	11	OUTPUT(2)	2nd Output	5,79(6,49)
4	GND	Ground	0(0)	12	GND	Ground	0(0)
5	GND	Ground	0(0)	13	GND	Ground	0(0)
6	OUTPUT(1)	1st Output	5,79(6,38)	14	INPUT-(2)	2nd Input(Negative)	0,5(0,5)
7	N.C.	No Connection	0(0)	15	N.C.	No Connection	0(0)
8	+V _s	Vcc; 12,5 V in this concept	12,41(13,83)	16	INPUT+(2)	2nd Input(Positive)	0(0)