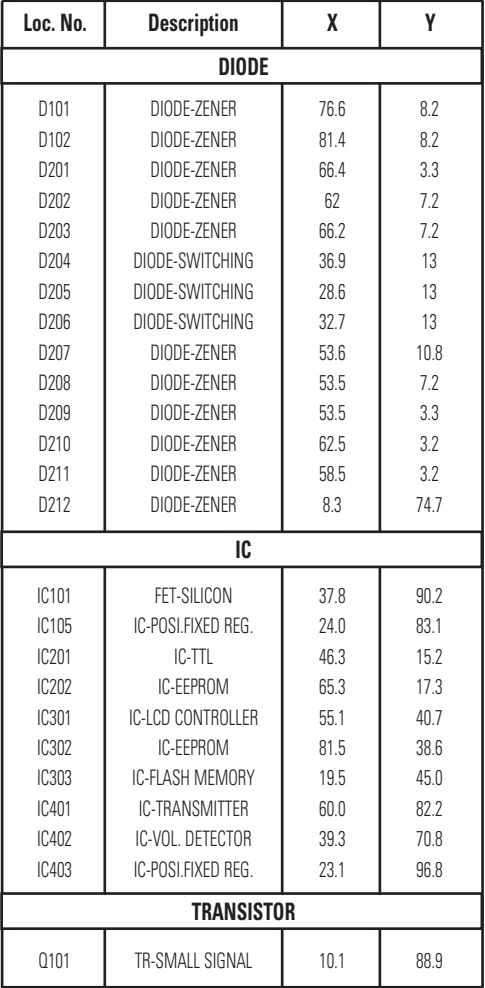


10-1 Main PCB Layout



Memo