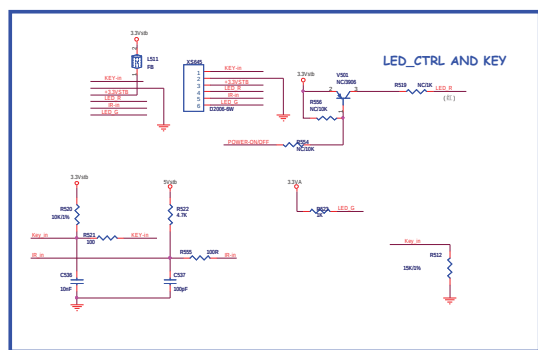
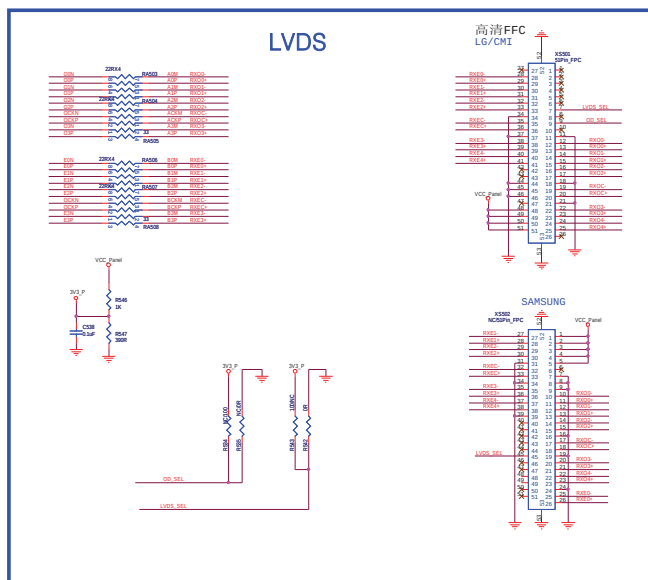
[illegible]

Audio MUTE Circuit

The diagram illustrates an Audio MUTE Circuit. It starts with a +12V supply connected to resistor R203 (390R). The other end of R203 is connected to the RESET# pin of the N205 IC (APX809-2.53). Resistor R201 (150) is connected between the RESET# pin and ground. The VCC pin of the N205 IC is connected to ground through resistor R242 (NC6R). The output of the N205 IC (pin 2) is connected to the AMP_RESET signal line through resistor R225 (0R). A 280ms pulse is shown at the AMP_RESET input. The AMP_RESET signal line is also connected to the base of transistor V214 (3904) through resistor R214 (NC14.7K). The emitter of V214 is connected to ground, and its collector is connected to the MUTE AMP signal line through resistor R213 (4.7K). The MUTE AMP signal line is also connected to the base of transistor V217 (3904) through resistor R216 (4.7K). The emitter of V217 is connected to ground, and its collector is connected to the AMP_EN signal line through resistor R217 (4.7K). Resistor R586 (10K) is connected between the AMP_EN signal line and the 3.3Vsb supply. Resistor R223 (1K) is connected between the AMP_EN signal line and ground. The 3.3Vsb supply is also connected to the base of V214 through resistor R214 (NC14.7K).

HI ==> MUTE
LO ==> UN-MUTE

[illegible]

Control

2 MUTE_AMP <<>> MUTE_AMP

2.6 SCL <<>> SCL

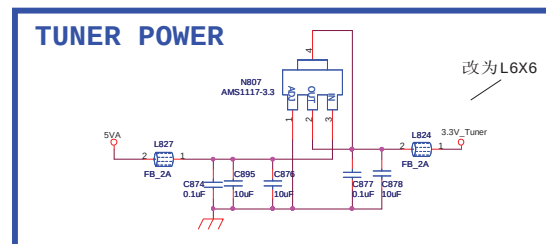
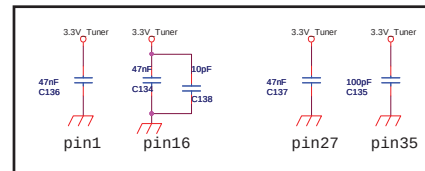
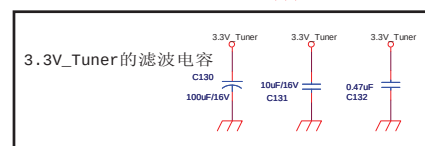
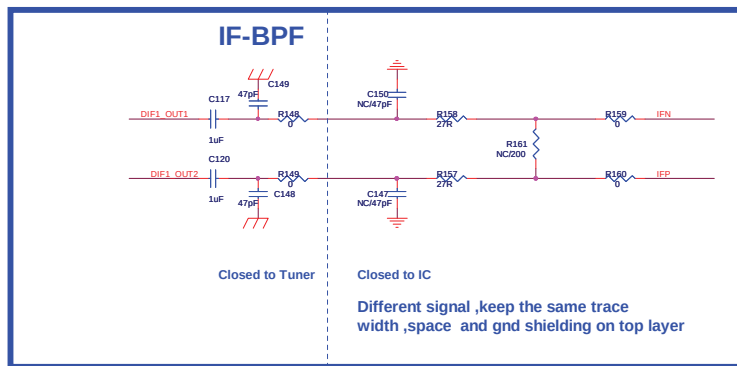
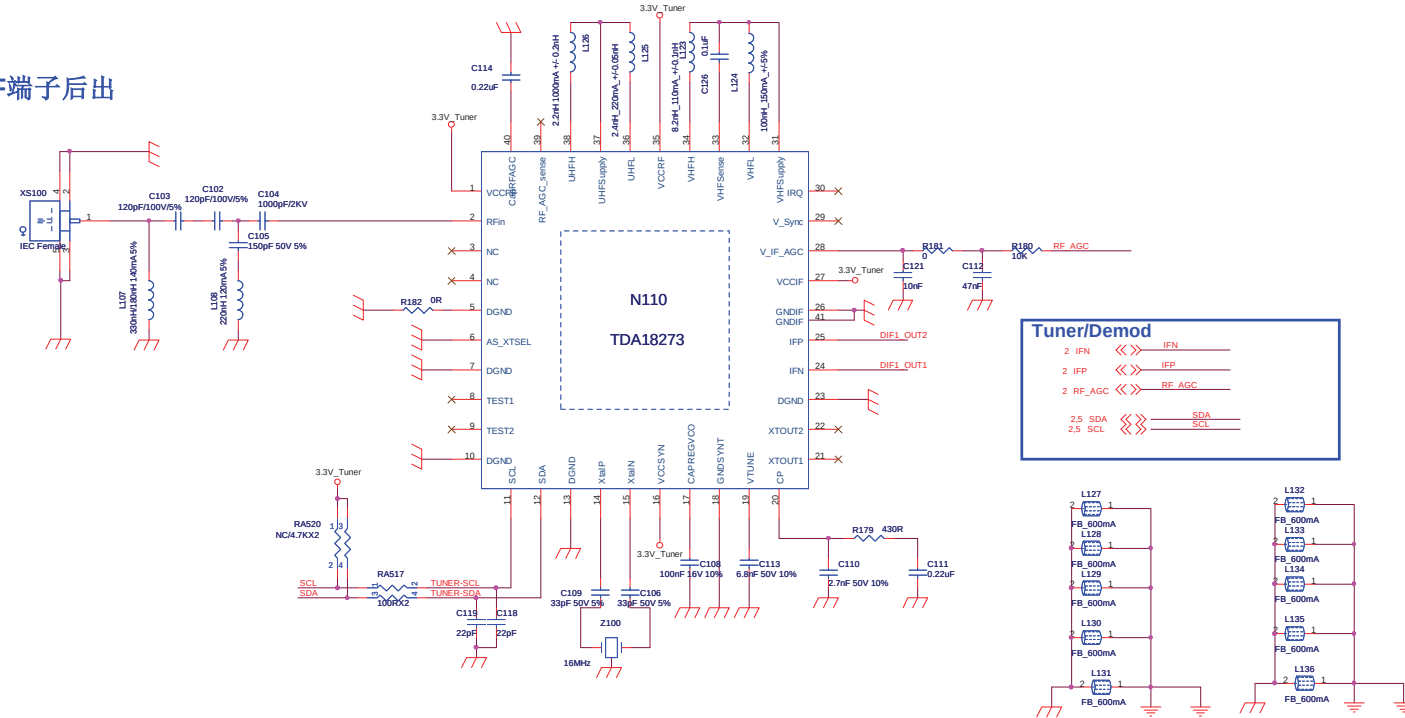
2.6 SDA <<>> SDA

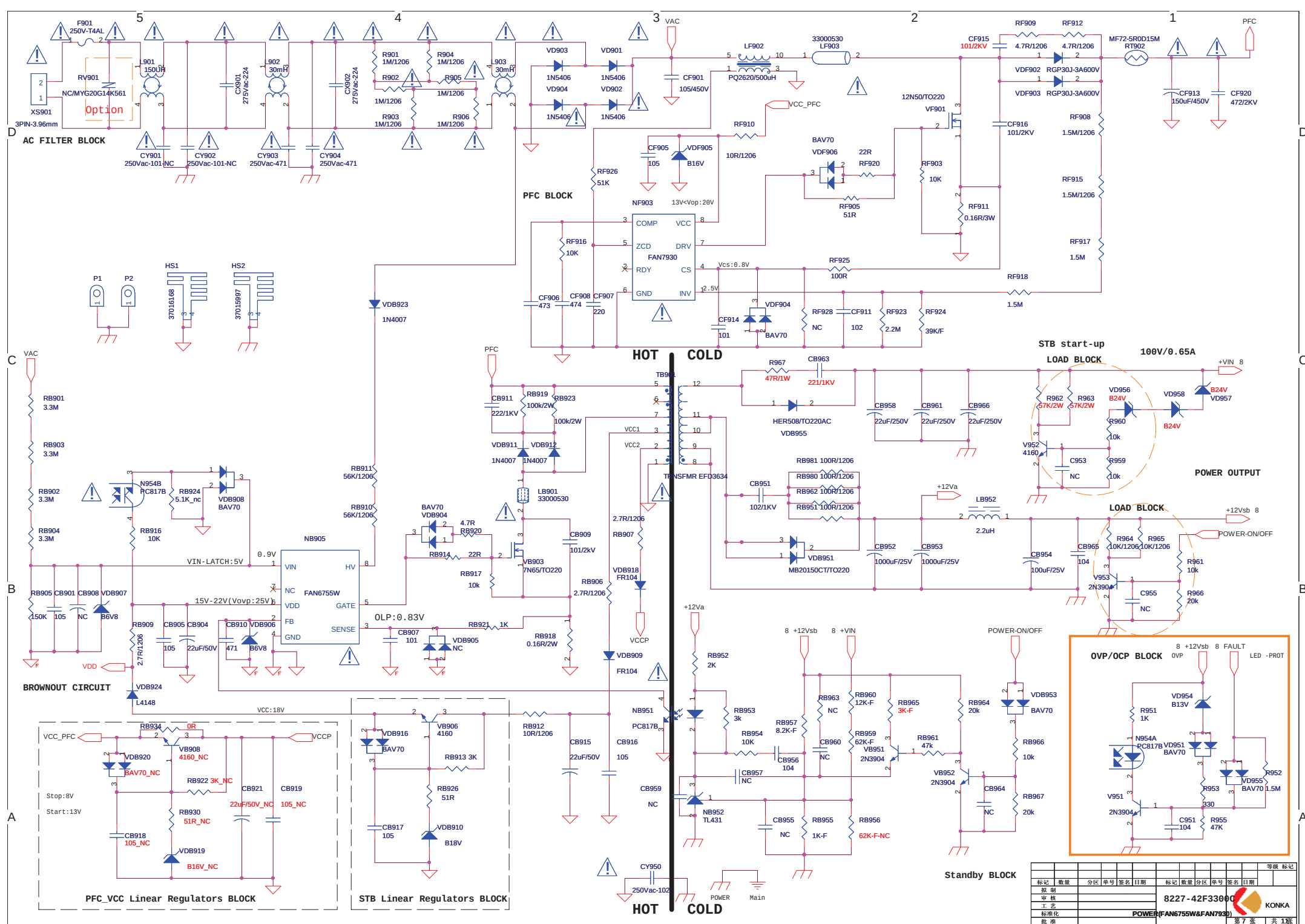
Audio

2 HP_L	《》	HP_L
2 HP_R	《》	HP_R
3 HPOUT_R	《》	HPOUT_R
3 HPOUT_L	《》	HPOUT_L
3 HP_MUTE	《》	HP_MUTE
2 AOMICK	《》	AOMICK
2 AOLRCK	《》	AOLRCK
2 AOBCCK	《》	AOBCCK
2 AOSDATA0	《》	AOSDATA0

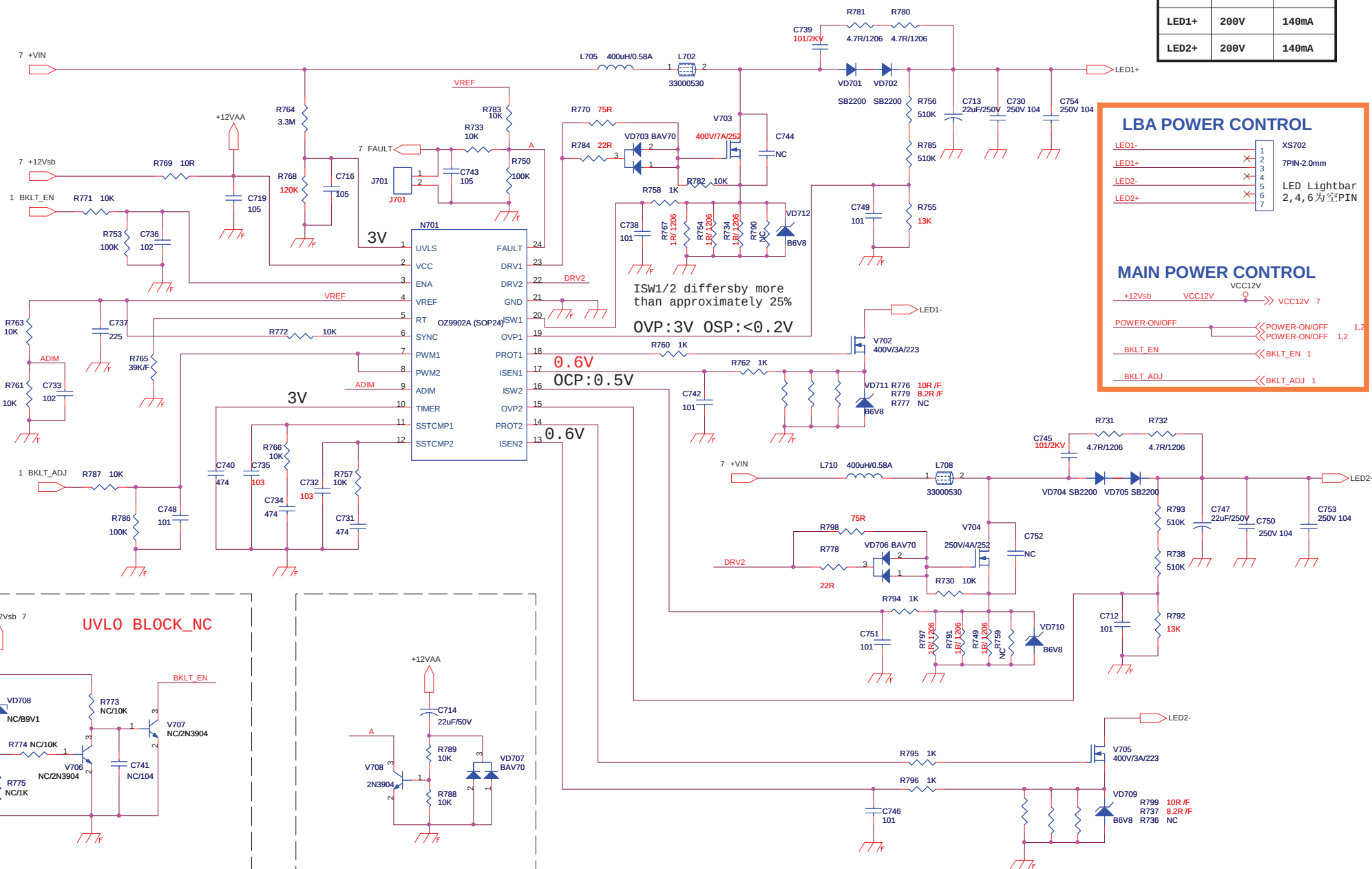
PDF 文件使用 "pdfFactory Pro" 试用版本创建 www.fineprint.com.cn

RF端子后出

[illegible]



Number	OutV	OutI
LED1+	200V	140mA
LED2+	200V	140mA



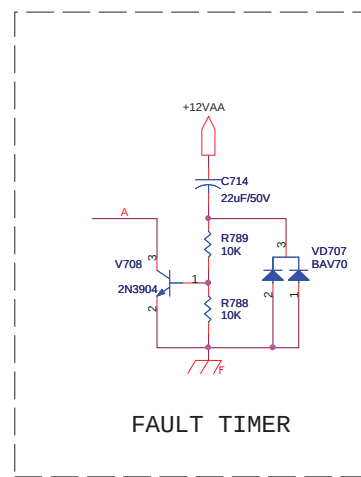
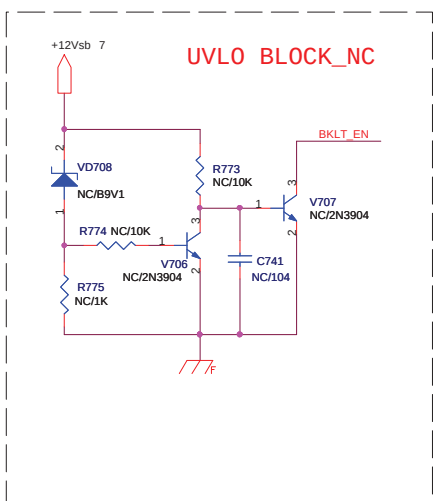
LBA POWER CONTROL

LED1-
LED1+
LED2-
LED2+

XS702
7PIN-2.0mm
LED Lightbar
2,4,6为空PIN

MAIN POWER CONTROL

+12Vsb VCC12V VCC12V 7
POWER-ON/OFF POWER-ON/OFF 1.2
BKLT_EN BKLT_EN 1
BKLT_ADJ BKLT_ADJ 1



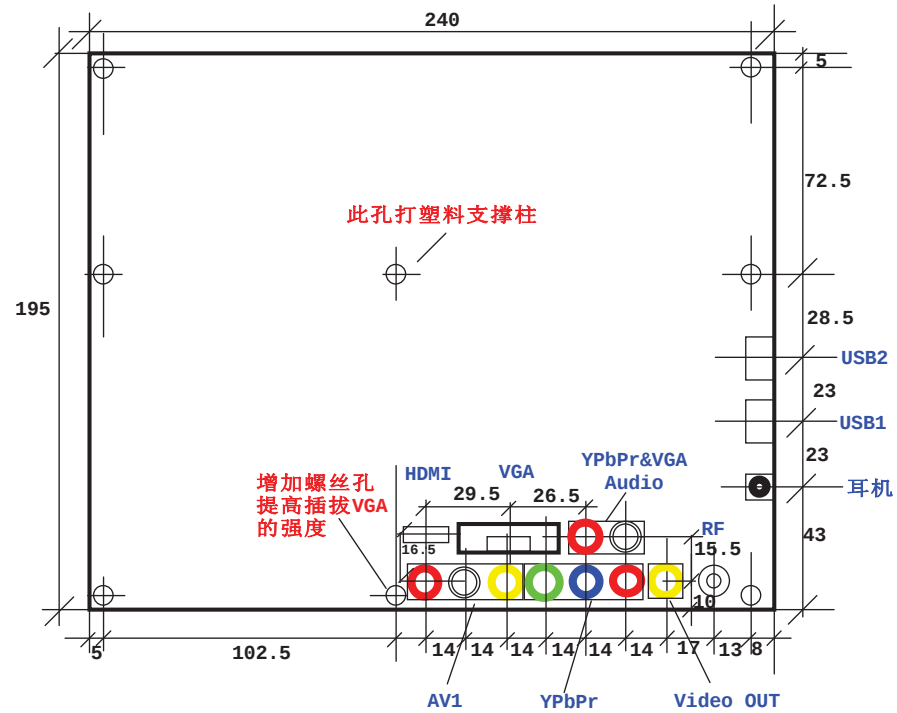
$F=6497K/RT$
如果ADIM大于1.5V,ILED=300/RSEN

标号	数量	分区	单号	签名	日期	标号	数量	分区	单号	签名	日期	等级	标记
审核						审核							
工艺						工艺							
标准化						标准化							
批准						批准							

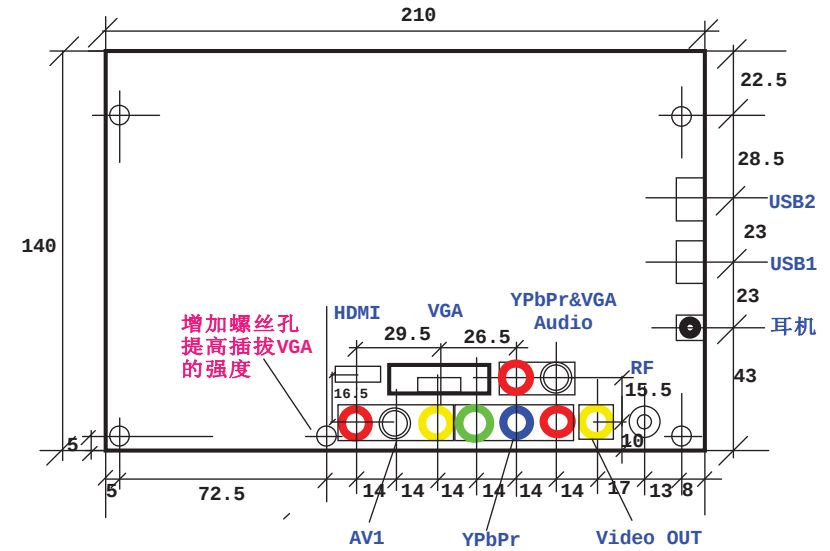
8227-42F3300C
POWER-LED Driver(OZ9902A)

第8张 共1张

LED40/42F3300C

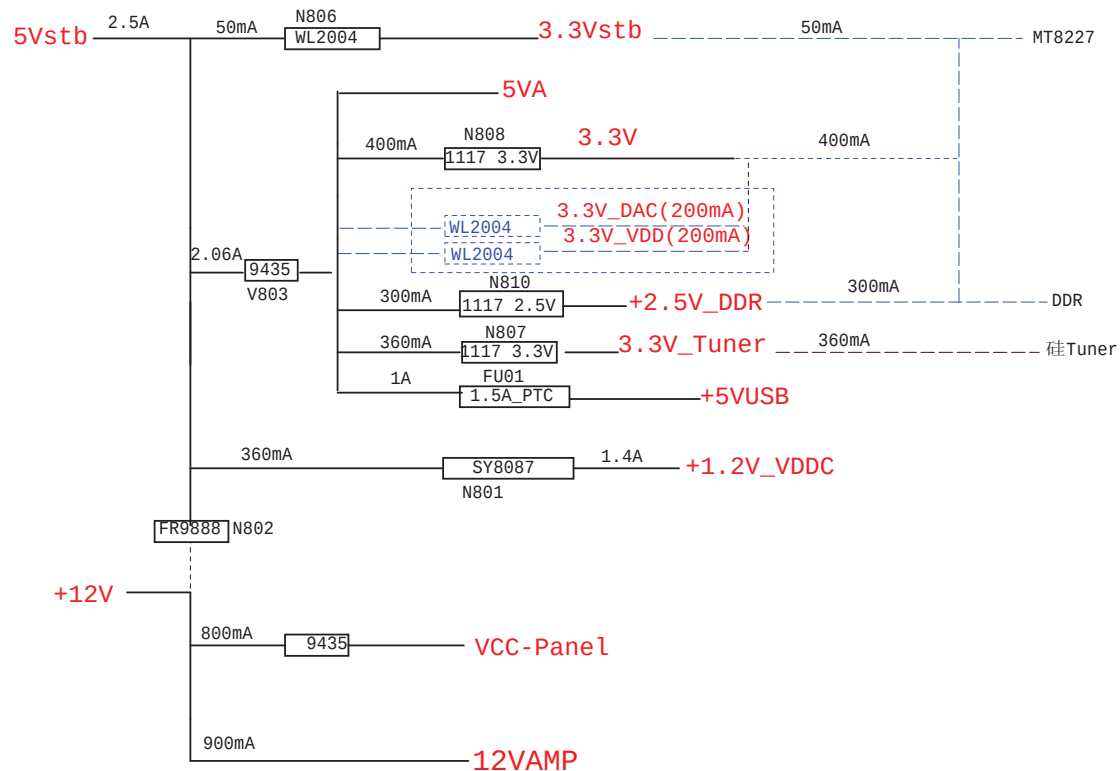


LED32F3300C



等级 标记									
标记	数量	分区	单号	签名	日期	标记	数量	分区	单号
拟制						SYS FLOW			
审核									
工艺									
标准化									
批准									





																	等级	标记
标记	数量	分区	单号	签名	日期	标记	数量	分区	单号	签名	日期							
拟制						<标题>		 KONKA										
审核																		
工艺																		
标准化																		
批准																		第 11 张 共 11 张

KONKA 康佳

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技术一流，无忧售后

